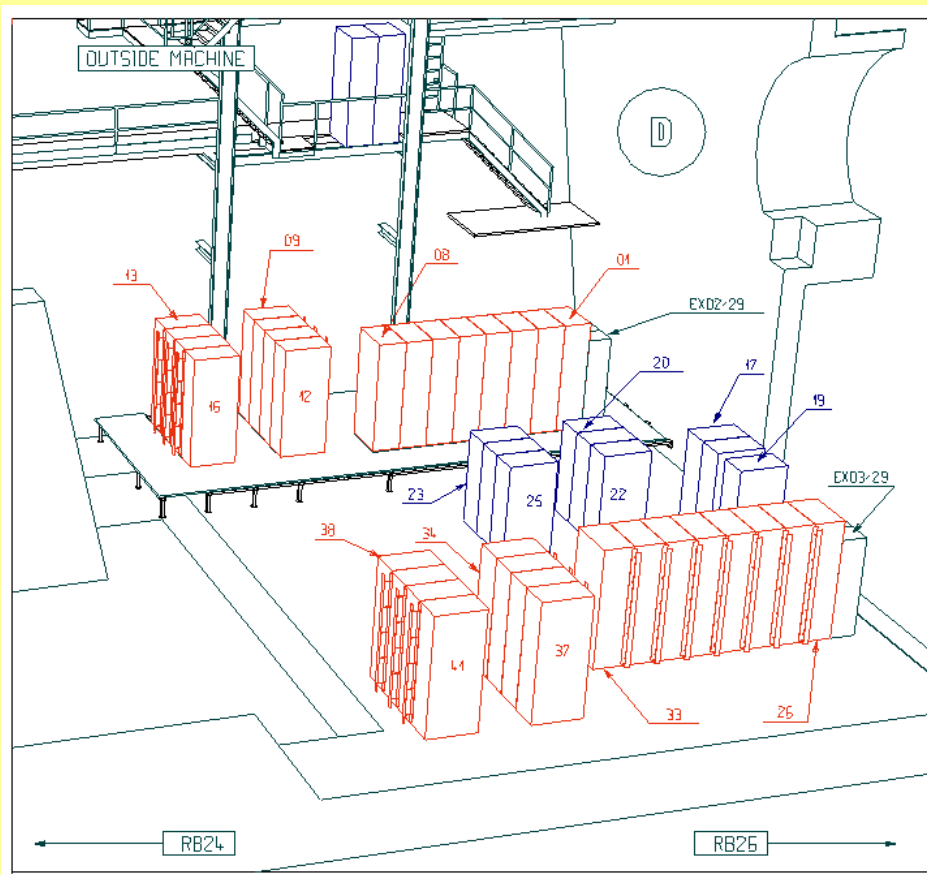


Racks for LHC Timing (Ch. Ilgner)



Rack layout, group D (under the muon platform)

ALICE Technical Note

EDMS No: 431316

ALICE-SER-SPC-0021, v. 2

Date of last change: 26/4/2005

ALICE Electronic Rack Layout ("Location and Allocation")

J.P. Vanuxem, PH-AIS

Function	Number of Racks	Rack Type	Rack Position
MTR-(LV, HV, CO)	6	LHC-52	D01-D03, D26-D28
TOF-(CO,CO-spare)	2	LHC-52	D04-D05
SPD-TR	1	LHC-52	D06
MCH-HV	2	LHC-52	D07, D32
MCH-LV	10	LHC-52	D08-D12, D33-D37
TPC-(CP, GP)	2	LHC-52	D15-D16
CTP	6	LHC-41	D17-D22
TRD-CT	3	LHC-41	D23-D25
LHC (timing)	2	LHC-52	D29-D30
Vacuum	1	LHC-52	D31
Racks to be installed	35	26 LHC-52 + 9 LHC-41	
Spare rack locations	5	5 LHC-52	D13-D14, D38-D40
Total	40 ^[1]	31 LHC-52 + 9 LHC-41	



questions J.-P. Vanuxem needs answers to:

1. exact type (eventually SCEM nr.)
2. magnetic field tolerance
3. heat dissipation inside the rack
4. total power consumption (\cong value in 3)

